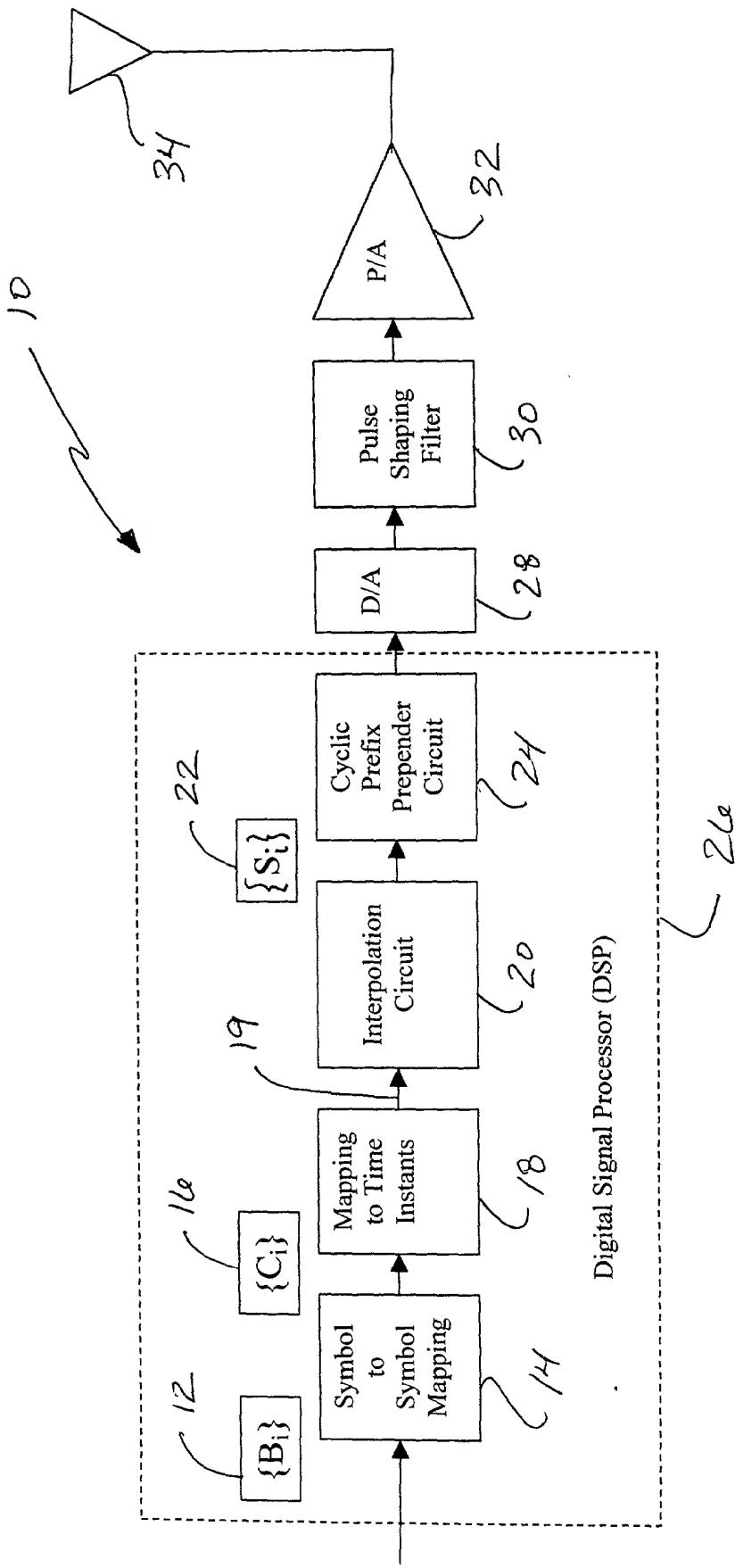


FIG. 1



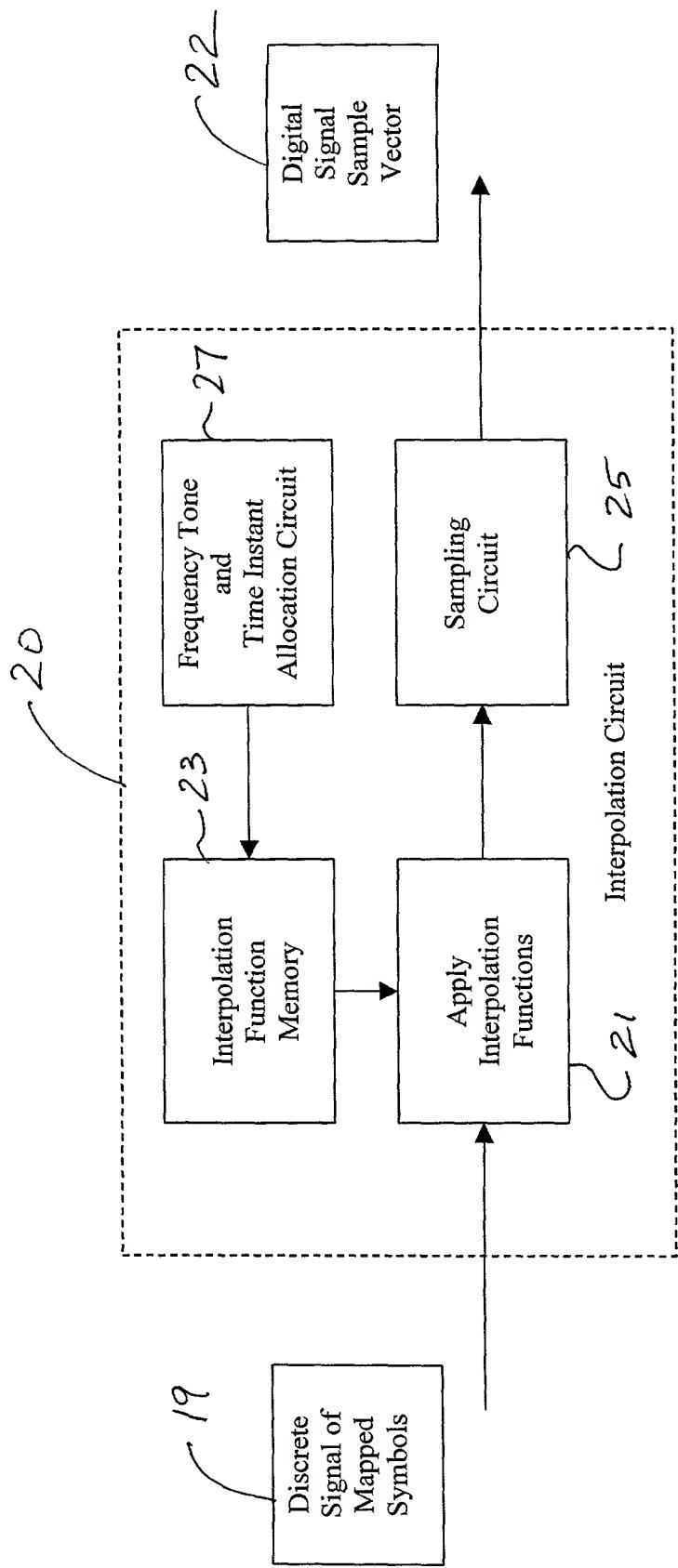
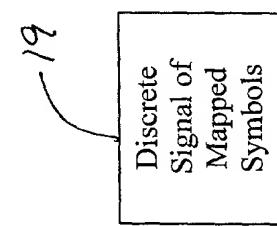


FIG. 2A

Discrete Signal of Mapped Symbols



Discrete Signal of
Mapped Symbols

Apply
Interpolation
Functions

Interpolation Circuit

Interpolation
Function
Memory

Frequency Tone
and
Time Instant
Allocation Circuit

Digital
Signal
Sample
Vector

20

23

27

21

FIG. 2B

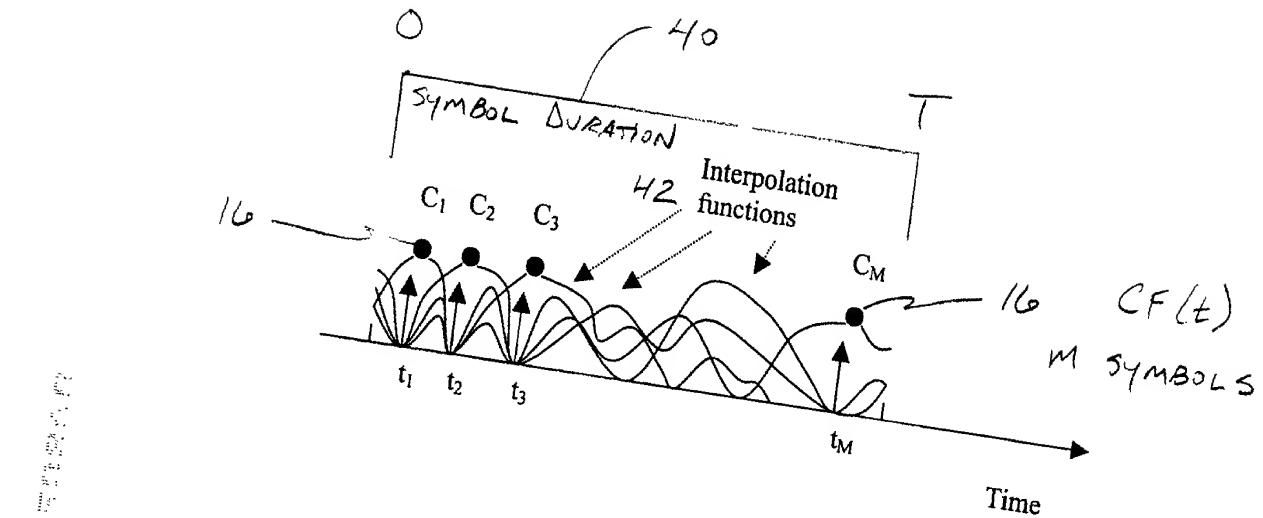


FIG. 3A

16
40
42
 $CF(t)$
 M SYMBOLS
Time

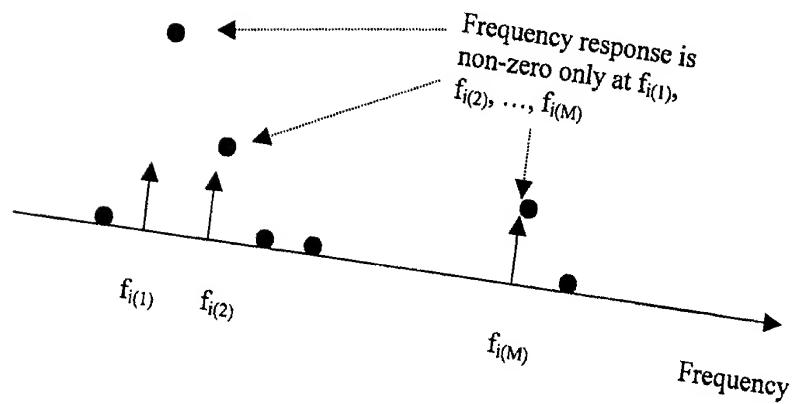


FIG. 3B

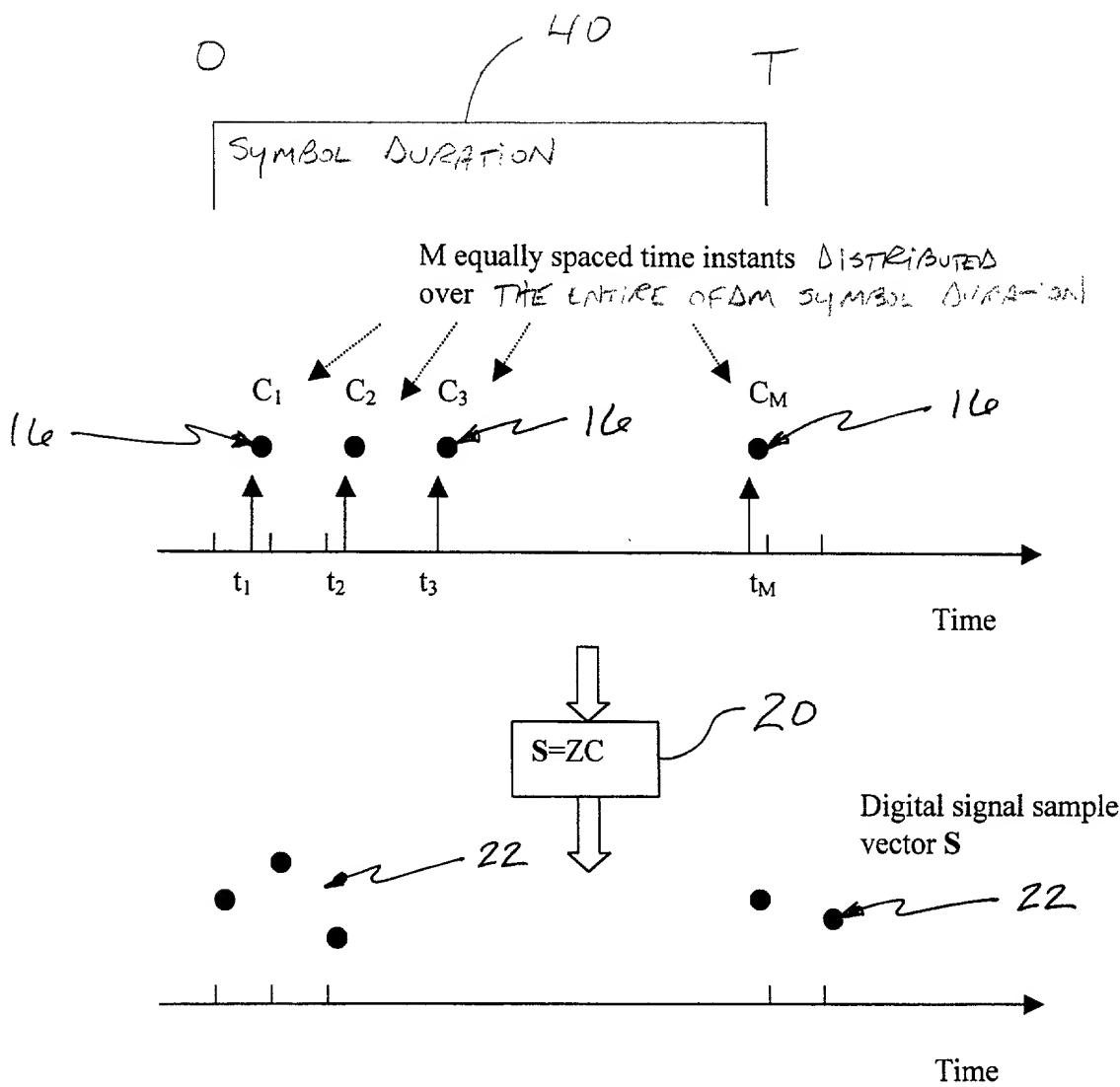


FIG. 4A

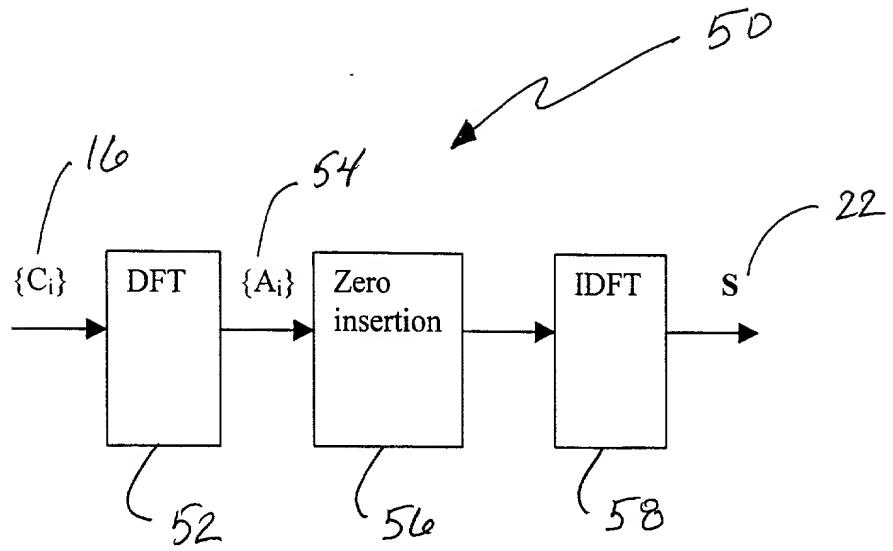


FIG. 4B

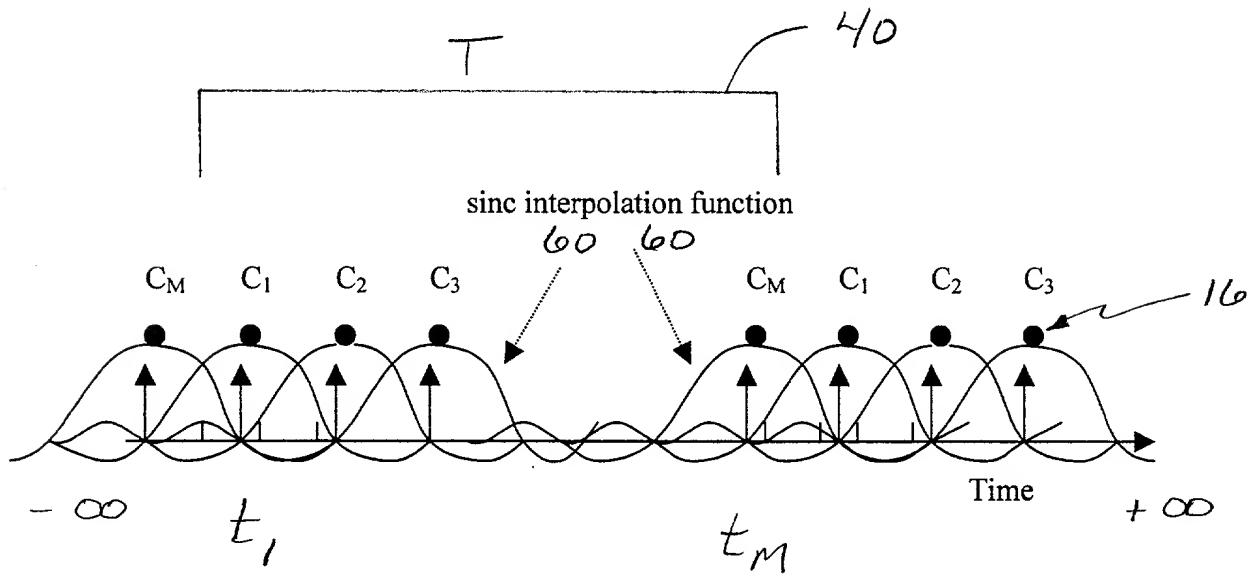


FIG. 4C

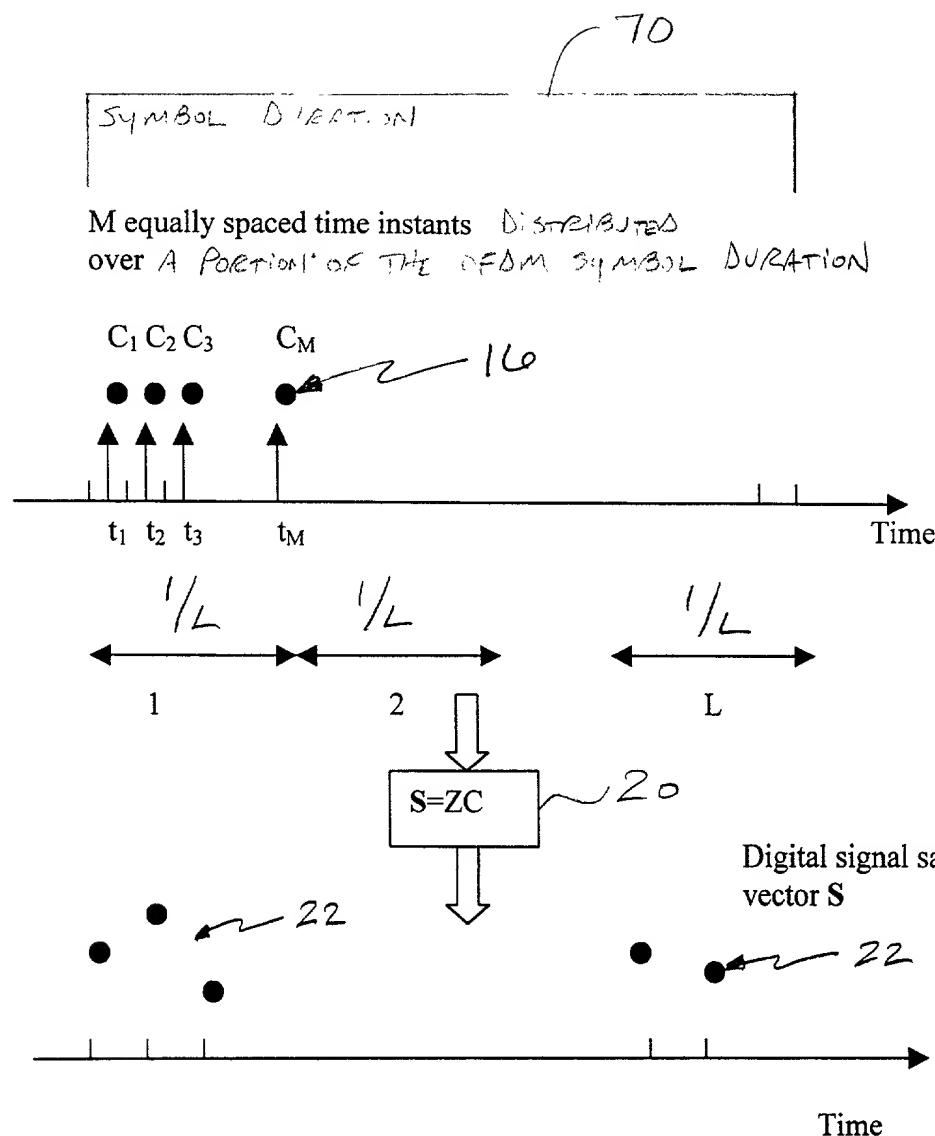


FIG. 5A

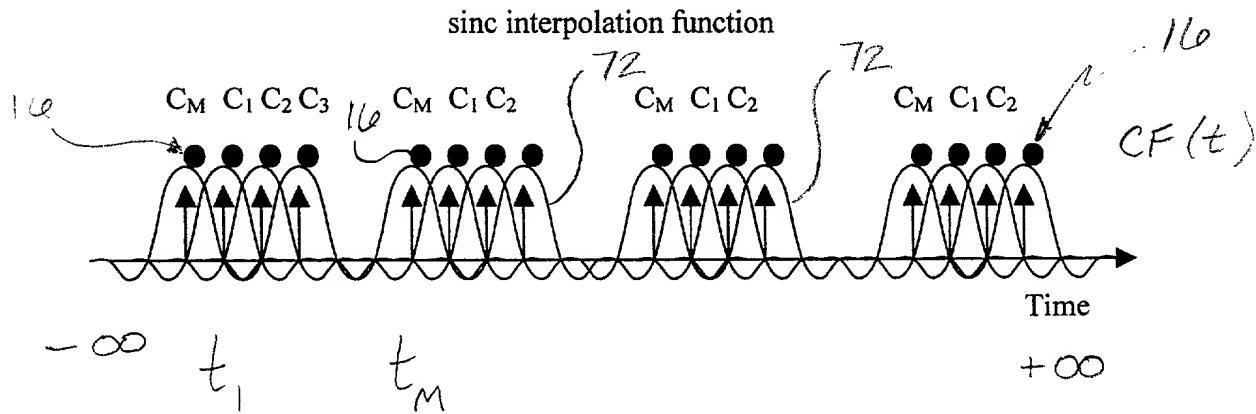


FIG. 5C

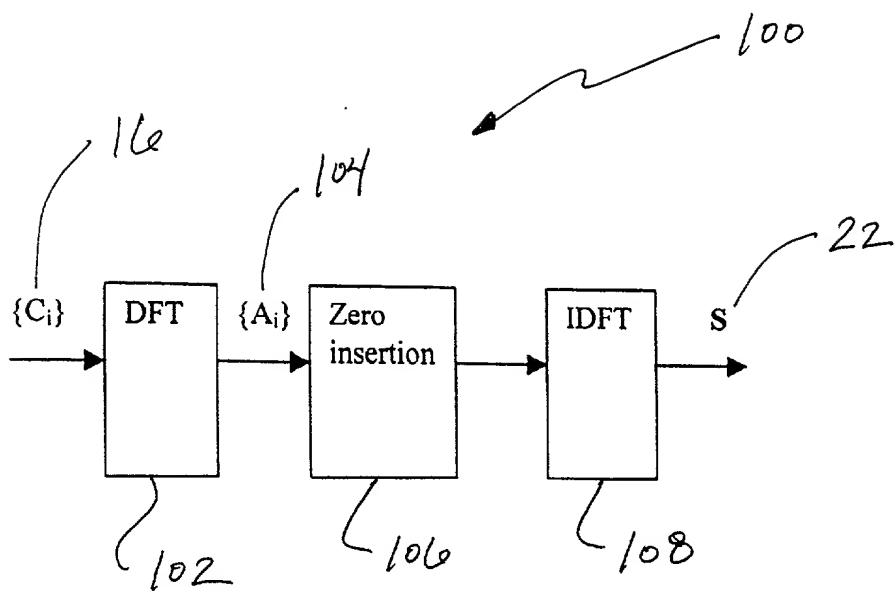


FIG. 5B

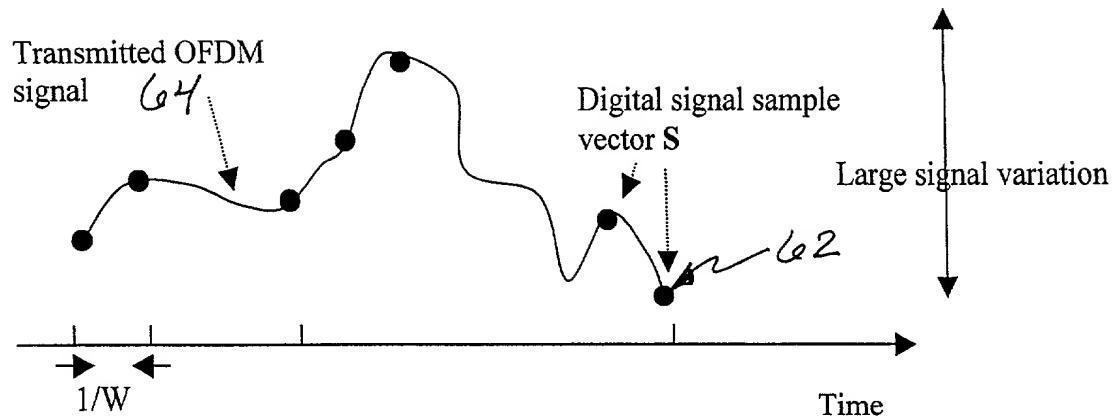


FIG. 4D

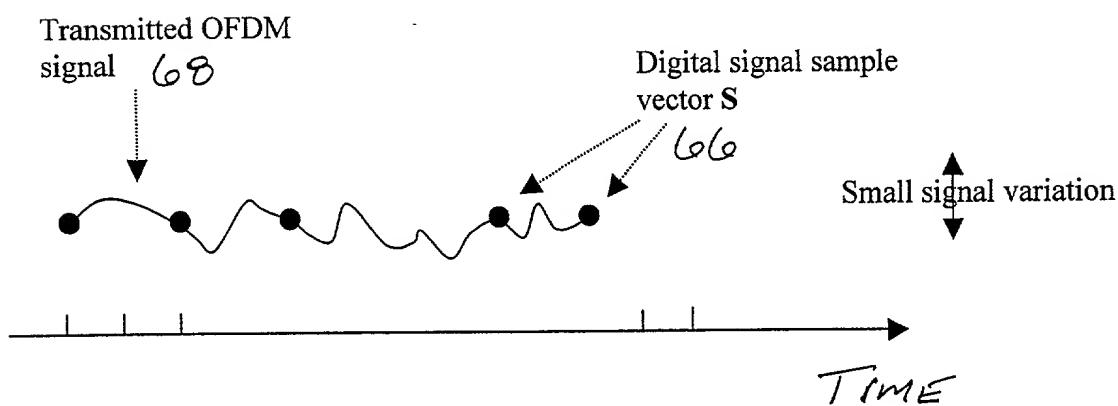


FIG. 4E

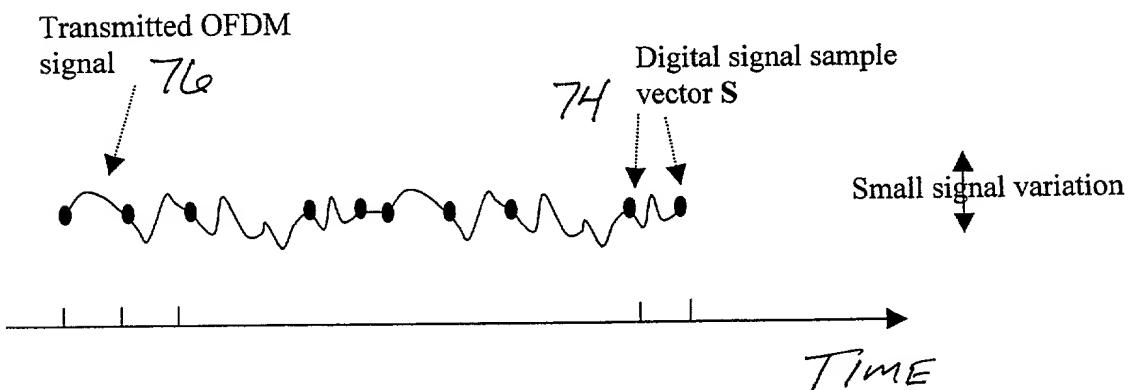


FIG. 5D

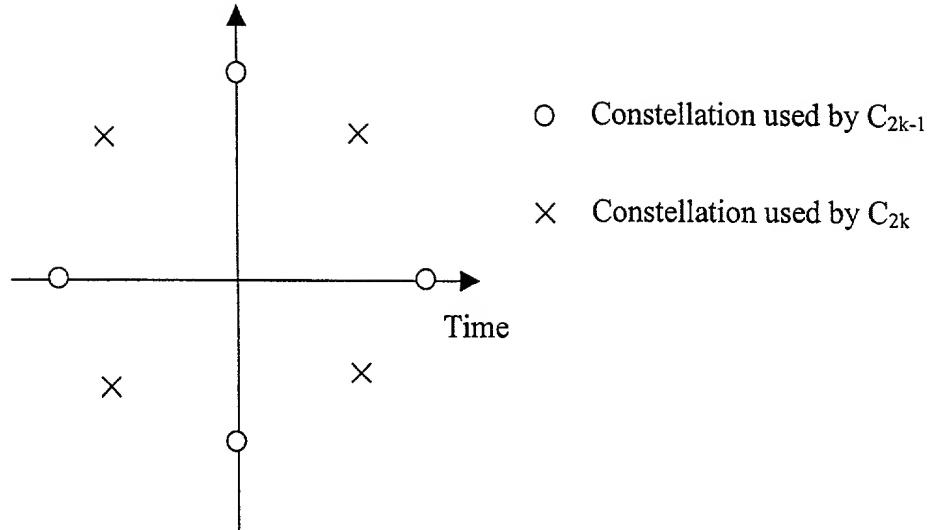
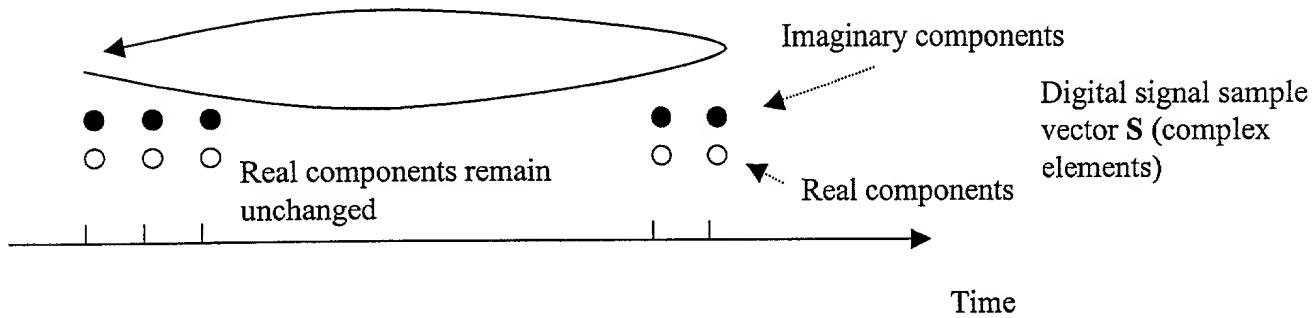


FIG. 6

Imaginary components cyclically *OFFSET*



Cyclic prefix is added after cyclically shifting

24

FIG. 7

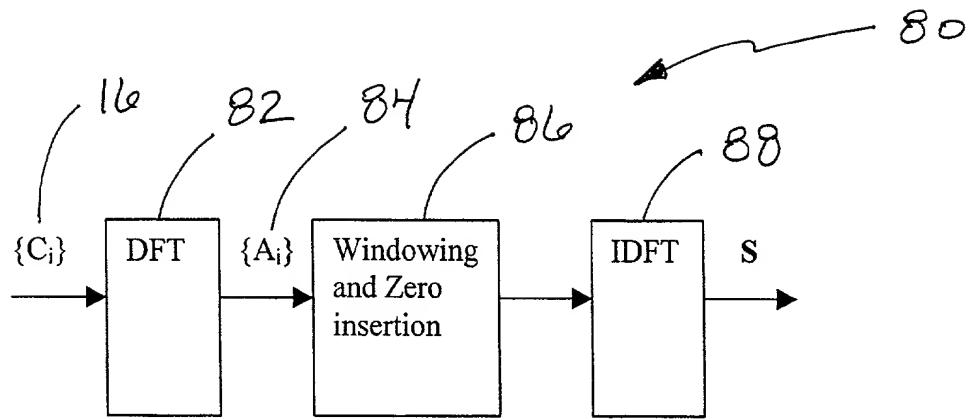


FIG. 8B

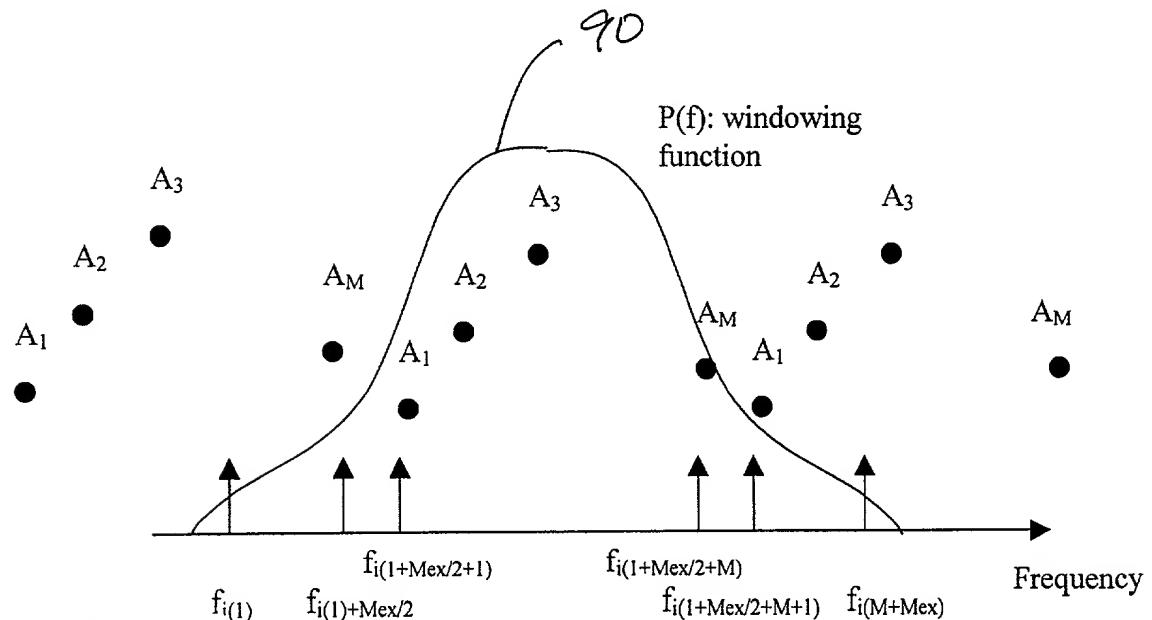


FIG. 8A

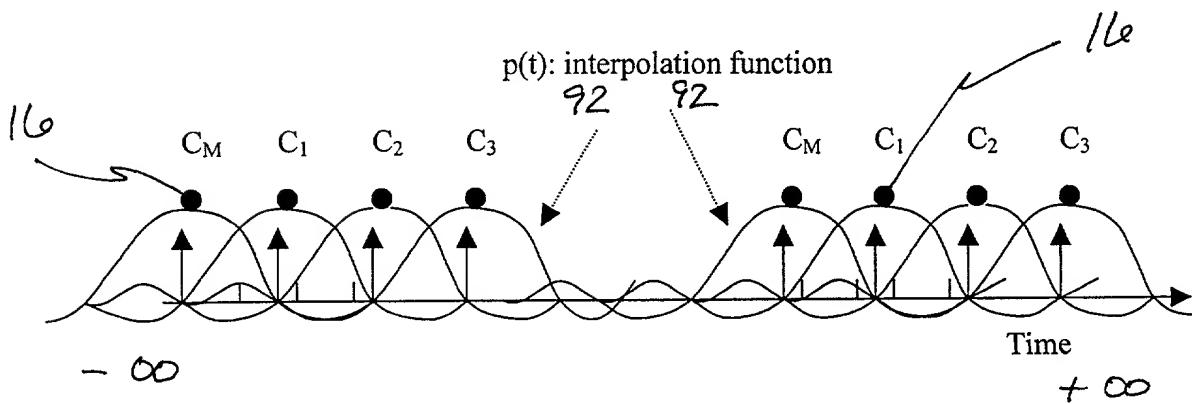


FIG. 8C

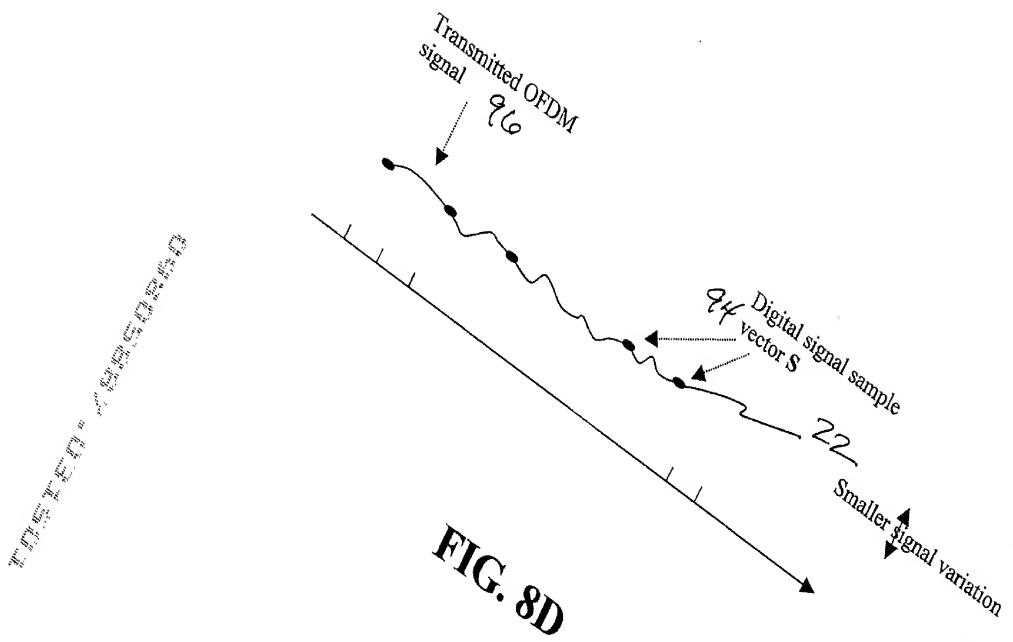


FIG. 8D